Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10	("3824597" "5146486" "5412665" "5485470" "6094459").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/07 12:49
S1	145	714/715.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/06 16:19
S2	2	"5485470".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/01/09 10:24
S3	2	"5485470".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/25 10:57
S4	305	(test$3) near transceiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/04 13:56
S5	66	((test$3) near transceiver).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/04 13:55
S6	82	((test$3) near transceiver)".ab.."	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/04 13:55
S7	78	((test$3) near transceiver) and (test near (pattern generator data bit input))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 14:57
S8	5	(((test$3) near transceiver) and (test near (pattern generator data bit input))) and (multiplexer and demultiplexer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 14:47

S9		7	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and . ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm. and 714/742. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S10		7	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm. and 714/738. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S11		7	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm. and 714/732. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S12		7	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm. and 714/729. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01

S13		7	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm. and 714/728. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S14		7	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm. and 714/724. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S15		15	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S16		79	((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:01
S17		33	((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:03

S18	3	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:04
S19	5	(((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)).ti. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)).ab. (((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/07 15:04
S20	34	((test$3) near transceiver) and ((test near (pattern generator data bit input)) LFSR "linear feedback shift register") and (multiplex$3 de-multiplex$3 demultiplex$3 "MUX" "DE-MUX" "DEMUX" interleav$3 deinterleav$3 de-interleav$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/14 17:22
S21	13	((test$3) near transceiver) and signature	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/14 17:42

S22	305	((test$3) near transceiver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/14 17:43
S23	4	((test$3) near transceiver) and ((multiplexer demultiplexer) near (test near (pattern data input signal)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/14 17:44
S24	402	(test$3 near transceiver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/06 16:19
S25	211	(test$3 near transceiver) and (test$3 near (pattern data generat$3 vector signal information))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/06 16:20
S26	74	(test$3 near transceiver) and (test$3 near (pattern data generat$3 vector signal information)) and (multiplexer demultiplexer "MUX" "DE-MUX" de-multiplexer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/06 16:22
S27	9	(test$3 near transceiver) and (test$3 near (pattern data generat$3 vector signal information)) and (multiplexer demultiplexer "MUX" "DE-MUX" de-multiplexer) and signature	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/06 16:26
S28	8	((test$3 near transceiver) with (pattern data generat$3 vector signal information) with plurality)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/06 16:27
S29	7	S28 not S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/06 16:27